

## Electronic Patent Application Fee Transmittal

Application Number:	09775167			
Filing Date:	01-Feb-2001			
Title of Invention:	Shift register circuit capable of reducing consumption of power with reduced capacitive load of clock signal line and image display device including it			
First Named Inventor/Applicant Name:	Yasushi Kubota			
Filer:	Steven M. Jensen			
Attorney Docket Number:	55561 (820)			
Filed as Large Entity				
<b>Utility Filing Fees</b>				
Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
<b>Basic Filing:</b>				
<b>Pages:</b>				
<b>Claims:</b>				
<b>Miscellaneous-Filing:</b>				
<b>Petition:</b>				
<b>Patent-Appeals-and-Interference:</b>				
Notice of appeal	1401	1	510	510
Post-Allowance-and-Post-Issuance:				
<b>Extension-of-Time:</b>				

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Extension - 3 months with \$0 paid	1253	1	1050	1050
<b>Miscellaneous:</b>				
<b>Total in USD (\$)</b>				<b>1560</b>